

5/20/03

To: RAJNIKANT PATEL

Re: 09/709,893 US 5,859,768

Attached are search results.

CP4-10A05

**No U. S. litigation was found in searches of
Lexis-Nexis and Questel-Orbit databases.**

**If more searching or explanation is needed, please
let me know.**

**Thanks,
Darcy Bates
STIC-EIC2800
306-5419
CP4-9C 18**

Bates, Darcy

From: Patel, Rajnikant
Sent: Tuesday, May 20, 2003 9:22 AM
To: STIC-EIC2800
Subject: LITIGATION SEARCH

Hi

Please send me a litigation search done for U.S. Patent # 5,859,768.

(Reissue Application # 09/709,893)

Thanks Raj. Patel

Day : Tuesday
Date: 5/20/2003

Time: 09:29:13

PALM INTRANET

Application Number Information

Application Number: 09/709893

Assignments

Filing Date: 11/13/2000

Effective Date: 11/13/2000

Application Received: 11/13/2000

Patent Number:

Issue Date: 00/00/0000

Date of Abandonment: 00/00/0000

Attorney Docket Number: SC10098C

Status: 30 /DOCKETED NEW CASE - READY FOR EXAMINATION

Confirmation Number: 4732

Title of Invention: **POWER CONVERSION INTEGRATED CIRCUIT AND METHOD FOR PROGRAMMING**

Examiner Number: 73406 / PATEL, RAJNIKANT

Group Art Unit: 2838

Class/Subclass: 363/021.000

Lost Case: NO

Interference Number:

Unmatched Petition: NO

L&R Code: Secrecy Code:1

Third Level Review: NO

Secrecy Order: NO

Status Date: 05/19/2003

Oral Hearing: NO

Bar Code	PALM Location	Location Date	Charge to Loc	Charge to Name	Employee Name	Location
09709893	28U4	05/19/2003	28X1	PATEL, RAJNIKANT	WESTIN,EDWARD	CP4/06/D 31

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Continuity Information for 09/709893

Parent Data

09709893

is a reissue of 08869297**Child Data**

No Child Data

Appln Info	Contents	Petition Info	Atty/Agent Info	Continuity Data	Foreign Data	Invent
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Time: 09:29:28

**PALM INTRANET****Application Number Information**Application Number: **08/869297** Order This
File AssignmentsExaminer Number: **73406 / PATEL, RAJNIKANT**Filing Date: **06/04/1997**Group Art Unit: **2838**Effective Date: **06/04/1997**Class/Subclass: **363/021.000**Application Received: **06/04/1997**Lost Case: **NO**Patent Number: **5859768**

Interference Number:

Issue Date: **01/12/1999**Unmatched Petition: **NO**Date of Abandonment: **00/00/0000**

L&R Code: Secrecy Code:1

Attorney Docket Number: **SC10098C**Third Level Review: **NO**Secrecy Order: **NO**Status: **150 /PATENTED CASE**Status Date: **12/22/1998**Confirmation Number: **5579**Oral Hearing: **NO**Title of Invention: **POWER CONVERSION INTEGRATED CIRCUIT AND METHOD FOR PROGRAMMING**

Bar Code	PALM Location	Location Date	Charge to Loc	Charge to Name	Employee Name	Location
08869297	9200	01/12/1999	No Charge to Location	No Charge to Name	LOE CONV DR1995C	

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Continuity Information for 08/869297

Parent Data

No Parent Data

Child Data

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Terms: **patno=5859768** ([Edit Search](#))

869297 (08) 5859768 January 12, 1999

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT
5859768

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January 12, 1999

Power conversion integrated circuit and method for programming

REISSUE: November 13, 2000 - Reissue Application filed Ex. Gp.: 2838; Re. S.N. 09/709,893 (O.G. March 4, 2003)

INVENTOR: Hall, Jefferson W., Phoenix, AZ; Alberkrack, Jade H., Tempe, AZ

APPL-NO: 869297 (08)

FILED-DATE: June 4, 1997

GRANTED-DATE: January 12, 1999

ASSIGNEE-AT-ISSUE: Motorola, Inc., Schaumburg, IL

ASSIGNEE-AFTER-ISSUE: June 4, 1997 - ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS)., MOTOROLA, INC. INTELLECTUAL PROPERTY DEPT. 1303 EAST ALGONQUIN ROAD SCHAUMBURG ILLINOIS 60196, Reel and Frame Number: 008585/0311
September 24, 1999 - SECURITY AGREEMENT, CHASE MANHATTAN BANK, THE, AS COLLATERAL AGENT 270 PARK AVENUE NEW YORK NEW YORK 10017, Reel and Frame Number: 010281/0057

April 24, 2000 - ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS)., SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC 5005 EAST MCDOWELL ROAD PATENT ADMINISTRATION DEPT. - A230 PHOENIX ARIZONA 85008, Reel and Frame Number: 010776/0122

June 11, 2002 - SUPPLEMENT TO SECURITY AGREEMENT, JPMORGAN CHASE BANK, AS COLLATERAL AGENT 270 PARK AVENUE NEW YORK NEW YORK 10017, Reel and Frame Number: 012991/0180

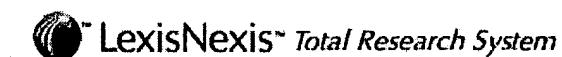
June 12, 2002 - SECURITY AGREEMENT, WELLS FARGO BANK MINNESOTA, NATIONAL ASSOCIATION, AS COLLATERAL AGENT 213 COURT STREET, SUITE 902 MIDDLETOWN CONNECTICUT 06457, Reel and Frame Number: 012958/0638

LEGAL-REP: Dover, Rennie William; Atkins, Robert D.

PUB-TYPE: January 12, 1999 - Utility Patent having no previously published pre-grant publication (A)

PUB-COUNTRY: United States (US)

US-MAIN-CL: 363#21.13

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patno=5859768

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5/20/03

US 5,859,768

09/709893

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Selected file: PLUSPAT

Search statement 2

?us5859768/pn

** SS 2: Results 1

Search statement 3

?prt fu legalall max

1/1 PLUSPAT - (C) QUESTEL-ORBIT- image
CPIM (C) Questel-Orbit
PN - US5859768 A 1990112 [US5859768]
TI - (A) Power conversion integrated circuit and method for programming
PA - (A) MOTOROLA INC (US)
PA0 - Motorola, Inc., Schaumburg IL [US]
IN - (A) HALL JEFFERSON W (US); ALBERKRACK JADE H (US)
AP - US86929797 19970604 [1997US-0869297]
PR - US86929797 19970604 [1997US-0869297]
IC - (A) H02H-003/20 H02M-003/24 H02M-003/335
EC - H02M-003/335C
PCL - ORIGINAL (O) : 363021130; CROSS-REFERENCE (X) : 361090000 363021160
363021180 363097000 363131000
DT - Basic
CT - US4980791; US5313381; US5335162; JP7325640
- SGS-Thomson Microelectronics Data Sheet, "VIPer100, SMPS Primary
I.C.", 14 pages.
STG - (A) United States patent
AB - A single input pin (48) provides multi-functional features for
programming a power supply (10). By connecting the appropriate
interface circuit (92, 100, or 112) to the single input pin (48), the
power supply (10) is programmed for specific behaviors during power up
and toggling of an on/off switch (96, 108). In one mode of operation a
light emitting diode (106) in the interface circuit (100) is optically
coupled to a microprocessor for signaling the closure of the on/off
switch (108), allowing the microprocessor to control the power supply
(10) through an opto-coupler (102). In another mode of operation, the
single on/off switch (96) controls the power supply (10). In yet
another mode of operation, Zener diode (118) in the interface circuit
(112) controls the power supply (10) during brown-out and black-out
conditions.

1/1 LGST - (C) LEGSTAT
PN - US 5859768 [US5859768]
AP - US 869297/97 19970604 [1997US-0869297]
DT - US-P
ACT - 19970604 US/AE-A
APPLICATION DATA (PATENT)
{US 869297/97 19970604 [1997US-0869297]}
- 19970604 US/AS02
ASSIGNMENT OF ASSIGNOR'S INTEREST
MOTOROLA, INC. INTELLECTUAL PROPERTY DEPT. 1303 EAST ALGONQUIN ROAD
SCHAUMBURG, * HALL, JEFFERSON W. : 19970530; ALBERKRACK, JADE H. :
19970530
- 19990112 US/A

5/20/03

US 5,859,768

09/709893

PATENT

- 20030304 US/RF
- REISSUE APPLICATION FILED
- 20001113

UP - 2003-11

1/1 CRXX - (C) CLAIMS/RRX

AN - 3097821
PN - 5,859,768 A 19990112 [US5859768]
PA - Motorola Inc
PT - E (Electrical)
ACT - 19990924 REASSIGNED
SECURITY AGREEMENT

Assignor: SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC DATE SIGNED:
08/04/1999

Assignee: CHASE MANHATTAN BANK, THE, AS COLLATERAL AGENT 270 PARK
AVENUE NEW YORK NEW YORK 10017

Reel 010281/Frame 0057

Contact: CRAVATH, SWAINE & MOORE CHERYLYN BRANDT WORLDWIDE PLAZA 825
8TH AVENUE NEW YORK, NEW YORK 10019

- 20000424 REASSIGNED
- ASSIGNMENT OF ASSIGNORS INTEREST

Assignor: MOTOROLA, INC. DATE SIGNED: 04/14/2000

Assignee: SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC 5005 EAST MCDOWELL
ROAD PATENT ADMINISTRATION DEPT. - A230 PHOENIX ARIZONA 85008

Reel 010776/Frame 0122

Contact: SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC ROBERT D. ATKINS
PATENT ADMINISTRATION DEPT-MD A230 5005 EAST MCDOWELL ROAD PHOENIX, AZ
85008

- 20001113 REISSUE REQUESTED
- ISSUE DATE OF O.G.: 20030304
- REISSUE REQUEST NUMBER: 09/709893
- EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2838

Reissue Patent Number:

- 20020611 REASSIGNED
- SUPPLEMENT TO SECURITY AGREEMENT

Assignor: SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC DATE SIGNED:
05/05/2002
SEMICONDUCTOR COMPONENTS OF RHODE ISLAND, INC. DATE SIGNED: 05/05/2002

Assignee: JPMORGAN CHASE BANK, AS COLLATERAL AGENT 270 PARK AVENUE NEW
YORK NEW YORK 10017

Reel 012991/Frame 0180

5/20/03

US 5,859,768

09/709893

Contact: FEDERAL RESEARCH CORPORATION PENELOPE AGODOA 400 SEVENTH STREET, NW, SUITE 101 WASHINGTON, DC 20004

- 20020612 REASSIGNED
SECURITY AGREEMENT

Assignor: SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC DATE SIGNED:
05/06/2002
SEMICONDUCTOR COMPONENTS INDUSTRIES OF RHODE ISLAND, INC. DATE SIGNED:
05/06/2002

Assignee: WELLS FARGO BANK MINNESOTA, NATIONAL ASSOCIATION, AS
COLLATERAL AGENT 213 COURT STREET, SUITE 902 MIDDLETOWN CONNECTICUT
06457

Reel 012958/Frame 0638

Contact: FEDERAL RESEARCH CORPORATION PENELOPE AGODOA 400 SEVENTH STREET, NW SUITE 101 WASHINGTON, DC 20004

UP - 2000-52
UACT- 2003-03-04
URAS- 2002-11-12

Search statement 3

?file inpadoc

Selected file: INPADOC

Search statement 2

?fam us5859768/pn

1 Patent Groups
** SS 2: Results 2

Search statement 3

?famstate nonstop

1/2 INPADOC - (C) INPADOC
PN - JP 11018423 A2 19990122 [JP11018423]
TI - POWER CONVERSION INTEGRATED CIRCUIT AND ITS PROGRAMMING METHOD
IN - HALL JEFFERSON W; JADE H ALBERKRACK
PA - MOTOROLA INC
AP - JP 170566/98-A 19980603 [1998JP-0170566]
PR - US 869297/97-A 19970604 [1997US-0869297]
IC - H02M-003/28

2/2 INPADOC - (C) INPADOC
PN - US 5859768 A 19990112 [US5859768]
TI - POWER CONVERSION INTEGRATED CIRCUIT AND METHOD FOR PROGRAMMING
IN - HALL JEFFERSON W [US]; ALBERKRACK JADE H [US]

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306-5419

5/20/03

US 5,859,768

09/709893

PA - MOTOROLA INC [US]
AP - US 869297/97-A 19970604 [1997US-0869297] .
PR - US 869297/97-A 19970604 [1997US-0869297]
IC - H02M-003/335; H02M-003/24; H02H-003/20

1/1 LEGALI - (C) LEGSTAT

PN - US 5859768 [US5859768]

AP - US 869297/97 19970604 [1997US-0869297]

DT - US-P

ACTE- 19970604 US/AE-A

APPLICATION DATA (PATENT)

{US 869297/97 19970604 [1997US-0869297]}

- 19970604 US/AS02

ASSIGNMENT OF ASSIGNOR'S INTEREST

MOTOROLA, INC. INTELLECTUAL PROPERTY DEPT. 1303 EAST ALGONQUIN ROAD
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19970530

- 19990112 US/A

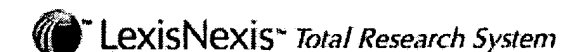
PATENT

- 20030304 US/RF

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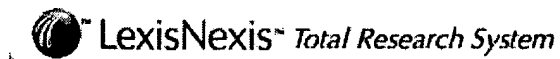
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